SUMMARY

- Electrical properties of Kapton hybrid was evaluated with full chips (n-ABCD) equipped.

- No major problem was found to Kapton itself, though...

  Oscillation at FE bias current of 100-200 μA

  (This problem is solved by using back- metallized chips and grounding)

- Gain, offset and noise were evaluated at FE bias current of 74 μA (also at other settings)

  (1) The noise is 0.05~0.09 fC for channels without wire bonding, and 0.12~0.20 fC for channels with wire bonding.

  The noise of this range increases with the chip number (chips 1~6).

  (2) The gain is uniform (~70 mV/fC). The rms variation of chips 1-6 is ~4% (~20% for chips 7-12 due to small Q-factor for these chips).

  (3) The offset is typically 40 mV.